

ABSTRACT OF THE DISCLOSURE

There is disclosed a method for reducing leakage current of an LSI, which enables information not memory-mapped in the address of a CPU to be easily saved, and information saving and returning to be carried out by simple switching operations without needing any special switching operations by the CPU. An LSI chip is divided into two parts, namely a main power supply region and a backup power supply region. A scan path is provided to interconnect memory units including a CPU, a CPU peripheral circuit and so on, in the main power supply region. When an operation standby state is set, a scanning operation through the scan path is started, information held in the memory units of each of the circuits in the main power supply region is read, and then thus read information is saved in an storage section in the backup power supply region.